Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	229	(712/15).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/10 19:58
L2	135	(712/20).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/10 19:58
L3	80	(712/21).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/10 19:58
L4	317	(712/22).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/10 19:58
L5	141	SIMD near4 MIMD near4 (hybrid\$1 or combin\$5 or parallel\$3 or simulatneous\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF .	2007/03/10 19:59
L6	56	SIMD near4 MIMD near4 (hybrid\$1 or combin\$5 or parallel\$3 or simulatneous\$3) near4 (instruction\$1 or operat\$5 or execut\$5)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR ·	OFF	2007/03/10 19:59
L7	15	SIMD near4 MIMD near4 (hybrid\$1 or combin\$5 or simulatneous\$3) near4 (instruction\$1 or operat\$5 or execut\$5)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/10 19:59
L8	75	(SIMD near4 MIMD) with (hybrid\$1 or combina\$5)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/10 20:11
L9	43	(SIMD near4 MIMD) with (hybrid\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/10 20:18
L10	6	(("5734921") or ("5754871") or ("5761523") or ("5828894") or ("5966528") or ("6094715")).PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/10 20:20
L11	. 124	manarray	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/10 20:20

			_			
L12	0	manarray with SIMD with MIMD	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/10 20:20
L13	0	manarray same (SIMD with MIMD)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/10 20:22
L14	. 7	manarray and (SIMD with MIMD)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/10 20:22
L15	3	((chen-inching\$) and (honary-hooman\$)).in.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/10 20:44
L16	41	((chen-inching\$) or (honary-hooman\$)).in.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/03/10 20:44
S1	209	(712/15).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 12:57
S2	135	(712/20).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 12:57
S3	78	(712/21).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 12:57
S4	285	(712/22).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 12:59
S5	96	SIMD with MIMD with switch\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 13:11
S6	31	SIMD with MIMD with switch\$3 with control\$4	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 13:01
S7	0	SIMIMD with switch\$3 with control\$4	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 13:01

S8	21	SIMIMD same (switch\$3 with control\$4)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 13:09
S9	31	SIMIMD	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR .	OFF	2006/05/02 13:09
S10	56	SIMD with MIMD with configur\$5	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 13:11
S11	48	SIMD near4 MIMD near4 configur\$5	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 13:11
S12	73	SIMD near4 MIMD near4 switch\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR .	OFF	2006/05/02 13:11
S13	0	SIMD near4 MIMD near4 configur\$5 near4 switch\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 13:18
S14	0	SIMD near4 MIMD near4 switch\$3 near4 mesh\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 13:12
S15	0	((SIMD near4 MIMD) or SIMIMD) with (switch\$3 near4 mesh\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 13:12
S16	0	((SIMD near4 MIMD) or SIMIMD) same (switch\$3 near4 mesh\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 13:13
S17	0	((SIMD with MIMD) or SIMIMD) same (switch\$3 near4 mesh\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 13:13
S18	0	((SIMD with MIMD) or SIMIMD) same (switch\$3 with mesh\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 13:13
S19	23	((SIMD with MIMD) or SIMIMD) same (mesh\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 13:13

S20	1	((SIMD with MIMD) or SIMIMD) with (mesh\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 13:13
S21	4	SIMD near4 control\$4 near4 switch\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 13:19
S22	79	SIMD with control\$4 with switch\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 14:07
S23	813	SIMD with control\$4 with process\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 13:19
S24	0	(gove-r).in.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 14:08
S25	226	(gove).in.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 14:08
S26	16	(gove).in. and (712/??).ccls.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 14:13
S27		("20050216700").PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/02 14:13



SIMD + MIMD + (hybrid or combination) + p Search

Advanced Scholar Search Scholar Preferences

Lowercase "or" was ignored. Try "OR" to search for either of two terms. [details]

Scholar All articles Recent articles Results 1 - 10 of about 1,910 for SIMD + MIMD + (hybrid or combination) + parallel execution. (0.11 seconds)

All Results

The GPA machine: a generally partitionable MSIMD architecture

Y Censor

T Bridges, DPS Inc, IN Bloomington - Frontiers of Massively Parallel Computation, 1990. ..., 1990 - ieeexplore.ieee.org

S Zenios

... once clearly separated, but hybrid architectures that ... This combination of generality and efficiency makes ... stream -- Multiple Data stream (SIMD) computers have ...

K Balmer

Cited by 43 - Related Articles - Web Search

N Ing-Simmons R Gove

Instruction execution trade-offs for SIMD vs. MIMD vs. mixed modeparallelism

TB Berg, HJ Siegel, SCS Inc, OR Beaverton - Parallel Processing Symposium, 1991. Proceedings., Fifth ... - ieeexplore.ieee.org

... execution In SIMD mode, performance of conditional statements, and the consequences

of switching parallelism modes. 1. Introduction Most large-scale parallel ...

Cited by 18 - Related Articles - Web Search

Massively parallel solutions for molecular sequence analysis - group of 17 »

B Schmidt, H Schroder, M Schimmler - Parallel and Distributed Processing Symposium., Proceedings ..., 2002 - ieeexplore.ieee.org

... Hybrid computing denotes the combination of the SIMD and ... We have built a hybrid

MIMD-SIMD architecture from general available ... v. 1.1.2. For the SIMD part we ...

Cited by 27 - Related Articles - Web Search

Dual mode SIMD/MIMD processor providing reuse of MIMD instruction memories as data memories when ... - group of 2

... NK Ing-Simmons, KM Guttag, RJ Gove, K Balmer - US Patent 5,239,654, 1993 - Google Patents

... "The Use of Parallel-Processing Computers in ... When the pro- cessors function in the

single instruction, multiple data mode (SIMD) the dedicated memories are ...

Cited by 113 - Related Articles - Web Search

[BOOK] Parallel optimization: theory, algorithms, and applications - group of 4 »

Y Censor, SA Zenios - 1997 - books.google.com

... Algorithm: A Computational Simplification 172 6.9.1 Hybrid algorithms for ... Following

Algorithm for Quadratic Programming 223 8.3 Parallel Matrix Factonization ...

Cited by 236 - Related Articles - Web Search - Library Search

Universal Mechanisms for Data-Parallel Architectures - group of 13 »

K Sankaralingam, SW Keckler, WR Mark, D Burger - ieeexplore.ieee.org

... wards providing a fine grain MIMD execution model for ... con-trolled, and operate in

parallel on different ... computation is similar to SIMD execution, except that ...

Cited by 13 - Related Articles - Web Search - BL Direct

Data management and control-flow aspects of an SIMD/SPMD parallellanguage/compiler - group of 5 »

MA Nichols, HJ Siegel, HG Dietz - Parallel and Distributed Systems, IEEE Transactions on, 1993 - ieeexplore.ieee.org

... and OPSILA [2], [3], [16] is a hybrid SIMDiSPMD machine. ... a combination of a C language

compiler and AWK scripts ... SIMD mode is the default mode of execution, and ...

Cited by 18 - Related Articles - Web Search

[BOOK] Parallel and Sequential Methods for Ordinary Differential Equations - group of 6 »

K Burrage - 1995 - books.google.com

... CMS, so that it primarily supports a SIMD data parallel ... The CMS is a hybrid machine

with each computation ... parallel domains, tiling or any combination ofthese. ...

Cited by 141 - Related Articles - Web Search - Library Search

instruction multiple data (SIMD) and multiple instruction multiple data (MIMD) modes and method of ... - group of 2 »

RJ Gove, K Balmer, NK Ing-Simmons, KM Guttag - US Patent 5,212,777, 1993 - Google Patents

... "A Single Board Image Computer with 64 Parallel Processors", by ... Each processor can

operate to execute the same instruction at the same time (SIMD mode) or ...

Cited by 88 - Related Articles - Web Search

The PASM project: a study of reconfigurable parallel computing - group of 3 »

HJ Siegel, TD Dietz, HG Kulaczewski, MB Maheswaran ... - Parallel Architectures, Algorithms, and Networks, 1996. ..., 1996 ieeexplore.ieee.org

... The syntax of ELP is based on C, extended to sup- port parallel execution. ELF currently includes constructs for SIMD and SPMD (single program - multiple data ... Cited by 10 - Related Articles - Web Search

Coocococo ogie >

12345678910 Next



SIMD + MIMD + (hybrid or combination) + e Search

Advanced Scholar Search
Scholar Preferences
Scholar Hole

Lowercase "or" was ignored. Try "OR" to search for either of two terms. [details]

Scholar All articles Recent articles Results 1 - 10 of about 1,910 for SIMD + MIMD + (hybrid or combination) + parallel execution. (0.11 seconds)

All Results

The GPA machine: a generally partitionable MSIMD architecture

Y Censor

T Bridges, DPS Inc, IN Bloomington - Frontiers of Massively Parallel Computation, 1990. ..., 1990 - ieeexplore.ieee.org

... once clearly separated, but hybrid architectures that ... This combination of generality

S Zenios and efficiency makes ... stream -- Multiple Data stream (SIMD) computers have ...

K Balmer N Ing-Simmons

Line Simmon

Cited by 43 - Related Articles - Web Search

R Gove

Instruction execution trade-offs for SIMD vs. MIMD vs. mixed modeparallelism

TB Berg, HJ Siegel, SCS Inc, OR Beaverton - Parallel Processing Symposium, 1991. Proceedings., Fifth ... - ieeexplore ieee.org ... execution In SIMD mode, performance of conditional statements, and the consequences

of switching parallelism modes. 1. Introduction Most large-scale parallel ...

Cited by 18 - Related Articles - Web Search

Massively parallel solutions for molecular sequence analysis - group of 17 »

B Schmidt, H Schroder, M Schimmler - Parallel and Distributed Processing Symposium., Proceedings ..., 2002 - ieeexplore.ieee.org

... Hybrid computing denotes the combination of the SIMD and ... We have built a hybrid

MIMD-SIMD architecture from general available ... v. 1.1.2. For the SIMD part we ... Cited by 27 - Related Articles - Web Search

<u>Dual mode SIMD/MIMD</u> processor providing reuse of MIMD instruction memories as data memories when ... - group of 2 »

NK Ing-Simmons, KM Guttag, RJ Gove, K Balmer - US Patent 5,239,654, 1993 - Google Patents

... "The Use of Parallel-Processing Computers in ... When the pro- cessors function in the

single instruction, multiple data mode (SIMD) the dedicated memories are ...

Cited by 113 - Related Articles - Web Search

[BOOK] Parallel optimization: theory, algorithms, and applications - group of 4 »

Y Censor, SA Zenios - 1997 - books.google.com

... Algorithm: A Computational Simplification 172 6.9.1 Hybrid algorithms for ... Following

Algorithm for Quadratic Programming 223 8.3 Parallel Matrix Factonization ...

Cited by 236 - Related Articles - Web Search - Library Search

Universal Mechanisms for Data-Parallel Architectures - group of 13 »

K Sankaralingam, SW Keckler, WR Mark, D Burger - ieeexplore.ieee.org

... wards providing a fine grain MIMD execution model for ... con- trolled, and operate in

parallel on different ... computation is similar to SIMD execution, except that ...

Cited by 13 - Related Articles - Web Search - BL Direct

Data management and control-flow aspects of an SIMD/SPMD parallellanguage/compiler - group of 5 »

MA Nichols, HJ Siegel, HG Dietz - Parallel and Distributed Systems, IEEE Transactions on, 1993 - ieeexplore.ieee.org

... and OPSILA [2], [3], [16] is a hybrid SIMDiSPMD machine. ... a combination of a C language

compiler and AWK scripts ... SIMD mode is the default mode of execution, and ...

Cited by 18 - Related Articles - Web Search

[BOOK] Parallel and Sequential Methods for Ordinary Differential Equations - group of 6 »

K Burrage - 1995 - books.google.com

... CMS, so that it primarily supports a SIMD data parallel ... The CMS is a hybrid machine

with each computation ... parallel domains, tiling or any combination ofthese. ...

Cited by 141 - Related Articles - Web Search - Library Search

... instruction multiple data (SIMD) and multiple instruction multiple data (MIMD) modes and method of ... - group of 2

<u>»</u>

RJ Gove, K Balmer, NK Ing-Simmons, KM Guttag - US Patent 5,212,777, 1993 - Google Patents

... "A Single Board Image Computer with 64 Parallel Processors", by ... Each processor can

operate to execute the same instruction at the same time (SIMD mode) or ...

Cited by 88 - Related Articles - Web Search

The PASM project: a study of reconfigurable parallel computing - group of 3 »

HJ Siegel, TD Dietz, HG Kulaczewski, MB Maheswaran ... - Parallel Architectures, Algorithms, and Networks, 1996 ..... 1996 - ieeexplore ieee.org

... The syntax of ELP is based on C; extended to sup- port parallel execution. ELF currently includes constructs for SIMD and SPMD (single program - multiple data ... Cited by 10 - Related Articles - Web Search

Goooooooogle ► ne: 12345678910 Next

Result Page:



SIMD + MIMD + (hybrid or combination) + sir Search

Advanced Scholar Search Scholar Preferences

Lowercase "or" was ignored. Try "OR" to search for either of two terms. [details]

Scholar All articles Recent articles Results 1 - 10 of about 621 for SIMD + MIMD + (hybrid or combination) + simultaneous execution. (0.05 seconds)

[BOOK] Parallel optimization: theory, algorithms, and applications - group of 4 » **All Results** 

Y Censor, SA Zenios - 1997 - books.google.com

Y Censor ... projections in the product space 112 5.9.3 The simultaneous multiprojections algorithm

K Balmer and ... of the Family of Bregman Functions 160 6.9 The Hybrid Algorithm: A ...

Cited by 236 - Related Articles - Web Search - Library Search R Gove

K Guttag

Simulating artificial neural networks on parallel architectures - group of 8 »

N Ing-Simmons NB Serbedzija - Computer, 1996 - ieeexplore.ieee.org

... a single input), and • weight parallelism (simultaneous weighted summa ... Coarse Fine Digital Analog Hybrid Optical (CM ... arrays PC boards Systolic SIMD SPMD Figure ...

Cited by 27 - Related Articles - Web Search - BL Direct

### Dual mode SIMD/MIMD processor providing reuse of MIMD instruction memories as data memories when ... - group of 2

NK Ing-Simmons, KM Guttag, RJ Gove, K Balmer - US Patent 5,239,654, 1993 - Google Patents Page 1. United States Patent Ing-Simmons et al. [54] DUAL MODE SIMD/MIMD PROCESSOR ... FIG. 9 (PRIOR ART) RECONFIGURABLE SIMD/MIMD LOCAL AND GLOBAL INTERCONNECTION ...

Cited by 113 - Related Articles - Web Search

#### Volume Visualisation on a Hybrid Parallel Architecture

B Schmidt, H Schröder - ntu.edu.sg

... Hybrid computing is the combination of the SIMD and MIMD paradigm ... In this paper we have presented a hybrid architecture, combining the SIMD and MIMD ...

Related Articles - View as HTML - Web Search

... instruction multiple data (SIMD) and multiple instruction multiple data (MIMD) modes and method of ... - group of 2 »

RJ Gove, K Balmer, NK Ing-Simmons, KM Guttag - US Patent 5,212,777, 1993 - Google Patents ... Gove et al. [54] MULTI-PROCESSOR RECONFIGURABLE IN SINGLE INSTRUCTION MULTIPLE DATA

(SIMD) AND MULTIPLE INSTRUCTION ... INTERCONNECT NETWORK SIMD 81 FIG. 7 FIG. 5 ...

Cited by 88 - Related Articles - Web Search

#### [BOOK] Parallel and Distributed Programming Using C++

T Hughes, C Hughes - 2003 - books.google.com

... S. 1 Coordinating Order of Execution 185 S. I. I ... 6.2.5.2 Using the MPMD (MIMD) Model

with PVM and ... The C++ Standard Library in combination with CORBA and the ...

Cited by 8 - Related Articles - Web Search - Library Search

#### Heterogeneous parallel and distributed computing - group of 4 »

VS Sunderam, G Geist - Parallel Computing, 1999 - ace.ual.es

... Coarse-grained MIMD machines are best matched for ... message passing, shared-memory,

and hybrid paradigms; thus ... The combination of these features allows parallel ...

Cited by 18 - Related Articles - View as HTML - Web Search

### A Grimm Collection of MIMD Fairy Tales

T Blank, JK Nicicolls - ieeexplore.ieee.org

... 4], and one can build hybrid machines [5 ... further distinguishes it from SIMD vector architectures. ... Execution Autonomy Addressing Autonomy Connection Autonomy I/O ...

Cited by 9 - Related Articles - Web Search

### [BOOK] Reporting Computational Experiments with Parallel Algorithms: Issues, Measures, and Experts' ...

RS Barr, BL Hickman - 1992 - faculty.smu.edu

... simultaneous pricing and pivoting, parallel pricing of ... of the performance of an MIMD parallel implementation ... dependent on a particular combination of algorithm .

Cited by 32 - Related Articles - View as HTML - Web Search - Library Search - BL Direct

#### Parallelism in Knowledge Discovery Techniques - group of 4 »

D Talia - Proceedings of the 6th International Conference on Applied ..., 2002 - Springer ... can be combined to form different hybrid paral- lelization ... Different combinations can raise different issues to be ... results on distributed memory MIMD machines. ... Cited by 3 - Related Articles - Web Search - BL Direct

> Gooooooooogle ▶ 12345678910 Next Result Page:

> > SIMD + MIMD + (hybrid or combina Searchs)